

Digital Signal Processing
Spring, 2024
Lecture: on M W F, 10:00 – 10:50 in Speare 117
Lab: on M W, 14:00 – 16:50 in Workman 187

Instructor: Sihua Shao

Office: Workman 209

Phone: (575)835-5932

E-mail: sihua.shao@nmt.edu

Office Hours: Appointment (anytime) by email and meet in-person or on Zoom

Teaching Assistant: Adrian Salustri

E-mail: adrian.salustri@student.nmt.edu

Teaching Assistant: Patrick Duane

E-mail: patrick.duane@student.nmt.edu

Course Description: Digital signal processing (DSP) uses a sequence of numbers to represent samples of a continuous variable in a domain such as time, space and frequency. Due to its flexibility and economical implementation, DSP has been adopted in a wide range of applications. This course will cover the principles of digital signal processing. Topics include: discrete-time signals and systems, frequency analysis of signals, z-transform, discrete and fast Fourier transform, infinite and finite impulse response filters, and implementation of real-time DSP algorithms on high performance 32-bit MCU.

Mode of Instruction: Face-to-face lectures on M W F, 10:00 - 10:50 in Speare 117. Lecture recordings are uploaded to Canvas (*Panopto Recordings*). Lecture notes written in Microsoft OneNote are exported to PDF and uploaded to Canvas (*Assignments/Lecture Notes*).

Pre-requisites: *EE 311 (Signals and Linear Systems) and EE 351 (Microcontroller)*

Place in Curriculum: This is a senior level required course for majors in Electrical Engineering.

Course Learning Outcomes:

After completion of this course, students are expected to be able to:

- Analyze discrete-time signals using Fourier series and Fourier transform.
- Analyze and design discrete-time systems using z-transform.
- Perform discrete Fourier transform of discrete-time signals.
- Design FIR and IIR filters to meet expected system specifications.
- Design difference equations to meet expected system specifications.
- Implement digital filters using MATLAB and on DSP board.
- Use computer and digital signal processor to create, analyze and process signals.
- Understand the limitations of digital signal processing.

Program Learning Outcomes: <https://www.nmt.edu/academics/eleceng/undergrad/index.php>

Course Requirements: (Either of the two textbooks is acceptable)

Textbook: *Proakis and Manolakis, Digital Signal Processing: Principles, Algorithms, and Applications, 5th Edition, Prentice Hall, 2021.*

Textbook: *Proakis and Manolakis, Digital Signal Processing: Principles, Algorithms, and Applications, 4th Edition, Prentice Hall, 2007.*

Grading:

• Homework: 20%	A	90-100	C	70-72
• 2 Midterms: 20% each	A-	86-89	C-	66-69
• Final: 20%	B+	83-85	D+	63-65
• Labs: 20%	B	80-82	D	60-62
	B-	76-79	F	<60
	C+	73-75		

Homework and exams: There will be 5 homework, 2 midterm exams and 1 final exam. Homework grading is more effort-based while exams require you to complete the problems correctly. In other words, exams are graded more harshly than homework. Homework will be available by the date when the needed materials have been covered in the courses. Homework will be due by one week after the available date, e.g., if HW 1 is available on Feb. 2, it is due by 11:59 pm on Feb. 8. Submission of homework will be done via Canvas online assignment portal. Exams will be done in the classroom on the scheduled dates. Students may work together on homework but must turn in individual assignments that **CANNOT BE IDENTICAL**. Late homework will not be accepted unless requested via email before the due date with a valid reason. Students must work on exams individually. There will be no make-up exams except in the case of extraordinary circumstances.

Labs: There will be a total of 8 labs. The materials will be available on Canvas one week before the lab starts. For example, Lab 1 starts on Jan. 29 and the materials for Lab 1 will be available on Canvas by Jan. 22. Each lab requires students to submit a report. In the report, students need to clearly explain how the lab is completed, observations and conclusions. The report will be due by 11:59 pm on Wednesday of the first week of the next lab (avoid procrastination), e.g., the first report is due by 11:59 pm on Feb. 7. Late report will not be accepted unless requested via email before the due date with a valid reason. There is no specific formatting requirement for the report. Students do not need to do check-in/out with the TA, and the lab grade only depends on the submitted reports. Students may work together on labs but must turn in individual reports that **CANNOT BE IDENTICAL**.

Lab lecture: On the first day of each lab, a brief lecture will be provided to review the background, principles and challenges of the lab assignments related to the topic.

Lecture Schedule:

Date	Chapter	Topic
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Jan. 17		Syllabus and Canvas Navigation
Jan. 19, 22, 24, 26	Chap. 1	Review of Signals and Systems
Jan. 29, 31 & Feb. 2	Chap. 2	Discrete-time Signals and Systems
Feb. 5, 7	Chap. 5	Frequency Response to Complex Exponential
Feb. 9, 12, 14, 16, 19	Chap. 3	Z-Transform
Feb. 21, 23	Chap. 5	Frequency-Selective Filter
Feb. 26		Midterm 1 Review
Feb. 28		Midterm 1
Mar. 1, 4, 6, 8	Chap. 4	Frequency Analysis of Signals
Mar. 11, 13, 15	Chap. 7	Discrete Fourier Transform
Mar. 18, 20, 22		Holiday
Mar. 25	Chap. 7	Discrete Fourier Transform
Mar. 27	Chap. 8	Fast Fourier Transform
Mar. 29		Holiday
Apr. 1	Chap. 8	Fast Fourier Transform
Apr. 3, 5	Chap. 9	Implementation of Discrete-time Systems
Apr. 8		Midterm 2 Review
Apr. 10		Midterm 2
Apr. 12, 15	Chap. 9	Implementation of Discrete-time Systems
Apr. 17, 19, 22, 24, 26	Chap. 10	Design of FIR and IIR Filters
Apr. 29 & May 1		Final Review

Lab Schedule:

Date	Lab Topic
Jan. 29, 31	Using FM4 Microcontroller with Keil MDK IDE
Feb. 5, 7	Generating a Tone
Feb. 12, 14	Data Acquisition
Feb. 19, 21, 26, 28	Aliasing
Mar. 4, 6, 11, 13	IIR Filters by Pole-Zero Placement
Mar. 25, 27 & Apr. 1, 3	FIR Filter Design (Using Windows)
Apr. 8, 10	Sounds effects
Apr. 15, 17, 22, 24, 29 & May 1	IIR Filter Design and Fixed-Point Implementations